

- SIL7 Package
- Efficiency up to 82 %
- Continuous Short Circuit Protection
- Ultra High I/O Isolation 5000 VAC with Reinforced Insulation, rated for 300 Vrms Working Voltage
- CMTI 65 kV/μs at 1 kV Common Mode Voltage
- Low Coupling Capacitance
- Operation Temperature Range -40 ~ 95 °C max.
- Suitable for IGBT Applications
- Option for Input / Output Clearance > 9 mm with Alternative Pinout

RoHS



Picture similar

Number Structure

RS7	-	05	05	S	10	A	5	(P)
Series / Package	Input Voltage	Output Voltage	Output Type	Power	Int. Code	Isolation Voltage	Pinning	
RS7 = SIL7	05 = 5.0 Vdc 12 = 12 Vdc 15 = 15 Vdc 24 = 24 Vdc	03 = 3.3 Vdc 05 = 5.0 Vdc 09 = 9.0 Vdc 12 = 12 Vdc 15 = 15 Vdc	S = Single Output D = Dual Output T = Dual Split (+15 /−9 Vdc)	10 = 1 W	Logistics Code	5 = 5000 Vac	P = Optional Special Pinning 2 mm Pin pitch	

v2 = 2nd Version

Model Selection Guide								
Model Number	Input Voltage Range (VDC)	Input Current		Output Voltage (VDC)	Output Current		Efficiency @ FL (% , typ.)	Capacitive Load @ FL (μF, max.)
		No-Load (mA, max.)	Full Load (mA, typ.)		Min. Load (mA)	Full Load (mA)		
RS7-0503S10A5v2	4.5 ~ 5.5	50	253.17	3.3	0	303	79	2200
RS7-0505S10A5v2	4.5 ~ 5.5	50	250	5	0	200	80	1000
RS7-0509S10A5v2	4.5 ~ 5.5	50	243.91	9	0	111	82	1000
RS7-0512S10A5v2	4.5 ~ 5.5	50	243.91	12	0	83.3	82	470
RS7-0515S10A5v2	4.5 ~ 5.5	60	253.17	15	0	66.7	79	470
RS7-1203S10A5v2	10.8 ~ 13.2	30	106.84	3.3	0	303	78	2200
RS7-1205S10A5v2	10.8 ~ 13.2	30	104.17	5	0	200	80	1000
RS7-1209S10A5v2	10.8 ~ 13.2	30	102.89	9	0	111	81	1000
RS7-1212S10A5v2	10.8 ~ 13.2	30	100.41	12	0	83.3	83	470
RS7-1215S10A5v2	10.8 ~ 13.2	30	102.89	15	0	66.7	81	470
RS7-1503S10A5v2	13.5 ~ 16.5	20	83.34	3.3	0	303	80	2200
RS7-1505S10A5v2	13.5 ~ 16.5	25	82.31	5	0	200	81	1000
RS7-1509S10A5v2	13.5 ~ 16.5	25	81.31	9	0	111	82	1000
RS7-1512S10A5v2	13.5 ~ 16.5	25	82.31	12	0	83.3	81	470
RS7-1515S10A5v2	13.5 ~ 16.5	25	82.31	15	0	66.7	81	470
RS7-2403S10A5v2	21.6 ~ 26.4	20	55.56	3.3	0	303	75	2200
RS7-2405S10A5v2	21.6 ~ 26.4	20	54.83	5	0	200	76	1000
RS7-2409S10A5v2	21.6 ~ 26.4	20	52.75	9	0	111	79	1000
RS7-2412S10A5v2	21.6 ~ 26.4	20	53.42	12	0	83.3	78	470
RS7-2415S10A5v2	21.6 ~ 26.4	20	54.12	15	0	66.7	77	470
RS7-0503D10A5v2	4.5 ~ 5.5	50	250	±3.3	0	±151	80	±1000
RS7-0505D10A5v2	4.5 ~ 5.5	50	243.91	±5	0	±100	82	±470
RS7-0509D10A5v2	4.5 ~ 5.5	50	238.1	±9	0	±55.5	84	±470
RS7-0512D10A5v2	4.5 ~ 5.5	50	243.91	±12	0	±41.7	82	±220
RS7-0515D10A5v2	4.5 ~ 5.5	60	253.17	±15	0	±33.3	79	±220
RS7-1203D10A5v2	10.8 ~ 13.2	30	104.17	±3.3	0	±151	80	±1000
RS7-1205D10A5v2	10.8 ~ 13.2	30	102.89	±5	0	±100	81	±470
RS7-1209D10A5v2	10.8 ~ 13.2	30	99.21	±9	0	±55.5	84	±470
RS7-1212D10A5v2	10.8 ~ 13.2	30	99.21	±12	0	±41.7	84	±220
RS7-1215D10A5v2	10.8 ~ 13.2	30	101.63	±15	0	±33.3	82	±220
RS7-1503D10A5v2	13.5 ~ 16.5	25	83.34	±3.3	0	±151	80	±1000
RS7-1505D10A5v2	13.5 ~ 16.5	25	81.31	±5	0	±100	82	±470
RS7-1509D10A5v2	13.5 ~ 16.5	25	79.37	±9	0	±55.5	84	±470
RS7-1512D10A5v2	13.5 ~ 16.5	25	81.31	±12	0	±41.7	82	±220
RS7-1515D10A5v2	13.5 ~ 16.5	25	82.31	±15	0	±33.3	81	±220
RS7-2403D10A5v2	21.6 ~ 26.4	20	54.12	±3.3	0	±151	77	±1000
RS7-2405D10A5v2	21.6 ~ 26.4	20	54.12	±5	0	±100	77	±470
RS7-2409D10A5v2	21.6 ~ 26.4	20	51.45	±9	0	±55.5	81	±470
RS7-2412D10A5v2	21.6 ~ 26.4	20	52.75	±12	0	±41.7	79	±220
RS7-2415D10A5v2	21.6 ~ 26.4	20	52.75	±15	0	±33.3	79	±220
RS7-051509T10A5v2	4.5 ~ 5.5	50	240.97	+15 / -9	0	+33.3 / -55.6	83	+68 / -220
RS7-121509T10A5v2	10.8 ~ 13.2	30	101.63	+15 / -9	0	+33.3 / -55.6	82	+68 / -220
RS7-151509T10A5v2	13.5 ~ 16.5	30	87.72	+15 / -9	0	+33.3 / -55.6	76	+68 / -220
RS7-241509T10A5v2	21.6 ~ 26.4	25	55.56	+15 / -9	0	+33.3 / -55.6	75	+68 / -220

ALL SPECIFICATIONS ARE TYPICAL AT 25 °C, NOMINAL INPUT AND FULL LOAD UNLESS OTHERWISE NOTED.

Input Specifications					
Parameter	Conditions	Min.	Typ.	Max.	Unit
Input Voltage Range	5.0 Vdc Input	4.5	5.0	5.5	VDC
	12 Vdc Input	10.8	12	13.2	
	15 Vdc Input	13.5	15	16.5	
	24 Vdc Input	21.6	24	26.4	
Input Current (No Load)		See Table			mA
Input Current (Full Load)		See Table			mA
Input Filter		Capacitor			-
Input Reflected Ripple Current ⁽¹⁾			20		mApk-pk
Start up Time	Nominal Vin and constant resistive load		30		ms
Recommended input fuse (slow blow)	5.0 Vdc Input		0.4		A
	12 Vdc Input		0.2		
	15 Vdc Input		0.2		
	24 Vdc Input		0.1		
Note : 1. Measured with a simulated source inductance of 12 μH and a source capacitor Cin (47 μF, ESR < 1 Ω at 100 kHz).					

Output Specifications					
Parameter	Conditions	Min.	Typ.	Max.	Unit
Output Voltage Accuracy		-3		+3	%
Line Regulation	For 1% Vin Change	-1.2		+1.2	%
Load Regulation	From 10 % to 100 % Load			10	%
	From 0 % to 100 % Load			35	
Cross Regulation	Asymmetrical Load 25 % / 100 % for Dual	-5		+5	%
Ripple & Noise ⁽¹⁾	20 MHz Bandwidth			100	mVpk-pk
Short Circuit Protection		Continuous (Automatic Recovery)			-
Temperature Coefficient		-0.03		+0.03	%/°C
Maximum Capacitive Load	Minimum Vin and constant resistive load	See Table			-
Note : 1. Measured with a 0.1 μF MLCC.					

Absolute Maximum Ratings					
Parameter	Conditions	Min.	Typ.	Max.	Unit
Input Surge Voltage (100 ms)	5.0 Vdc Input			7	VDC
	12 Vdc Input			15	
	15 Vdc Input			18	
	24 Vdc Input			28	
Soldering Temperature	1.5 mm from case 10 sec. max.			260	°C
Note : These are stress ratings. Exposure of devices to any of these conditions may adversely affect long-term reliability.					

General Specifications						
Parameter	Conditions		Min.	Typ.	Max.	Unit
Efficiency			See Table			%
Isolation Voltage	Input-output, and rated for 60 sec.	Suffix “5”	5000			VAC
Isolation Resistance	Input-output		1000			MΩ
Isolation Capacitance	Input-output			10		pF
Leakage Current	250VAC, 60Hz			2.0		μA
Working Voltage					300	Vrms
Clearance / Creepage	2.54 mm Pin pitch		6			mm
	2 mm Pin pitch		9			
Overvoltage Category	2.54 mm Pin pitch		OV2			
	2 mm Pin pitch		OV3			
Common Mode Transient Immunity	1 kV Common mode Voltage		65			kV/μs
Switching Frequency			20			kHz
Reliability Calculated MTBF	MIL-HDBK-217 F @ 25 °C		2400			k hours
Insulation System			Reinforced Insulation			
Safety Standard	IEC / EN / UL 62368-1		Designed to meet			
Environmental compliance	RoHS		Designed to meet			

Environmental Specifications						
Parameter	Conditions		Min.	Typ.	Max.	Unit
Operating Ambient Temperature	See the Derating Curve		-40		95	°C
Maximum Case Temperature					115	°C
Thermal Impedance			30			°C/W
Operating Altitude					5000	m
Pollution Degree			PD2			
Storage Humidity					95	% rel. H
Storage Temperature			-55		125	°C
Cooling	Natural Convection		30 - 65 LFM			

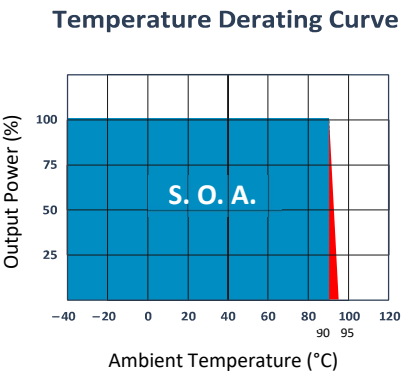
EMC Specifications			
Parameter	Standard	Condition	Criterion
Conducted Emissions	EN55032	with external components	B
Radiated Emissions	EN55032		B
ESD	IEC 61000-4-2	Air: ±15 kV / / Indirect: ±6 kV	A
RS	IEC 61000-4-3	10 V/m	A
EFT	IEC 61000-4-4	±2 kV with external components	A
Surge	IEC 61000-4-5	±2 kV with external components	A
CS	IEC 61000-4-6	10 Vrms	A
PFMF	IEC 61000-4-8	100 A/m	A

Physical Specifications	
Parameter	Value
Case Material	Nonconductive Black Plastic (UL94V-0 rated)
Pin Material	Tinned copper
Potting Material	Silicone (UL94V-0 rated)
Weight	4.3 g, typ.
Dimensions	0.77" x 0.39" x 0.49"

Electrical Characteristic Curves

Thermal Derating

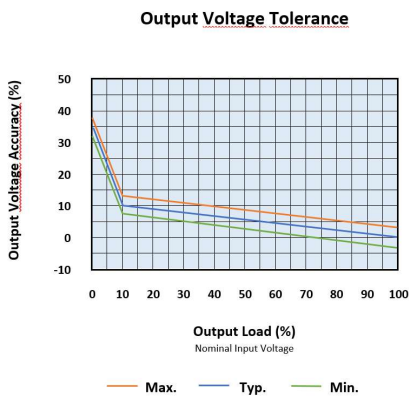
To enhance the system reliability, the power module should always be operated below the maximum operating temperature. If the temperature exceeds the maximum operating temperature, reliability of the unit may be affected.



All Models

Output Voltage Tolerance Envelope Curve

Output Voltage Tolerance Curve

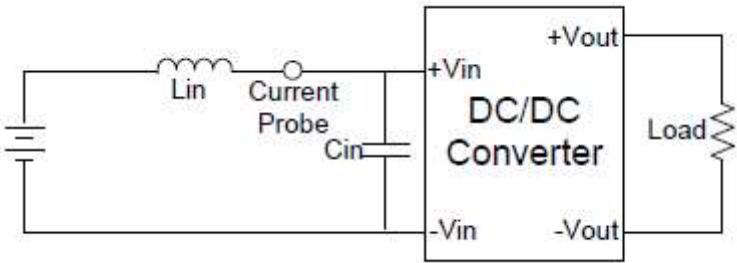


All Models

Test Configurations

Input Reflected Ripple Current Test Step

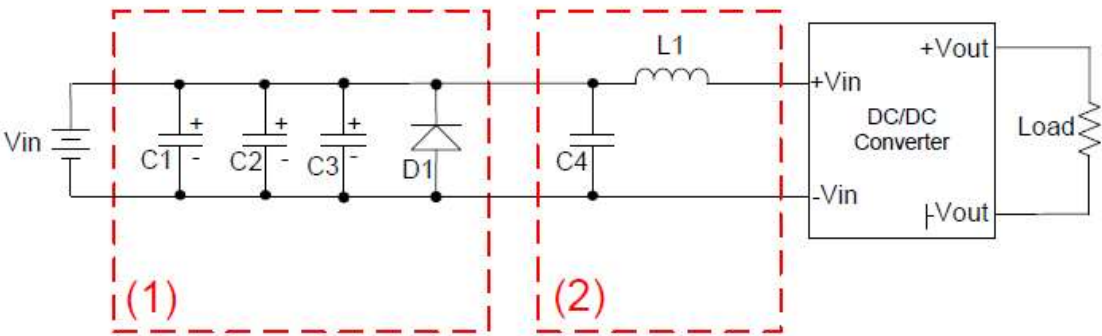
Input reflected ripple current is measured with a source inductor L_{in} (12 μH) and a source capacitor C_{in} (47 μF , ESR < 1.0 Ω at 100 kHz) at nominal input and full load.



EMC Filter Details

EMC Filter

The part (1) Circuit is used to meet Surge & EFT test, and the part (2) Circuit is used to meet EMI test.



V in	C1, C2	C3	D1	C4	L1
5 V	Nippon Chemi-con KY-Series 680μF / 100V	Nippon Chemi-con KY-Series 680μF / 100V	SMDJ8.0A	MLCC 4.7μF / 50V	6.8μH
12 V		DNP	SMDJ16.0A	MLCC 10μF / 50V	
15 V			SMDJ18.0A		
24 V			SMDJ28.0A		

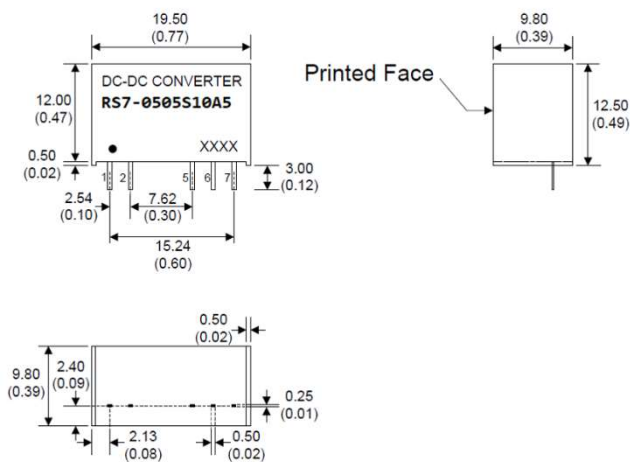
Pin Connections

Standard Pin pitch		
PIN NUMBER	SINGLE	DUAL
1	+Vin	+Vin
2	-Vin	-Vin
5	-Vout	-Vout
6	N.P.	COM
7	+Vout	+Vout
	*N.P. : No PIN	

2 mm Pin pitch		
PIN NUMBER	SINGLE	DUAL
1	+Vin	+Vin
2	-Vin	-Vin
7	-Vout	-Vout
8	N.P.	COM
9	+Vout	+Vout
	*N.P. : No PIN	

Mechanical Specifications

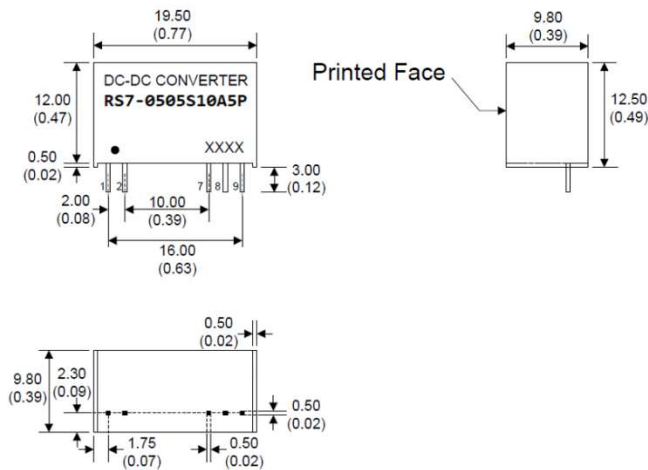
Standard Pin pitch



- Notes : All dimensions are typical in millimeters (inches).
- 1. Pin dimension tolerance: ± 0.05 (± 0.002)
 - 2. Pin pitch and length tolerance: ± 0.35 (± 0.014)
 - 3. Pin to case tolerance: ± 0.5 (± 0.02)
 - 4. Case Tolerance: ± 0.5 (± 0.02)

Mechanical Specifications

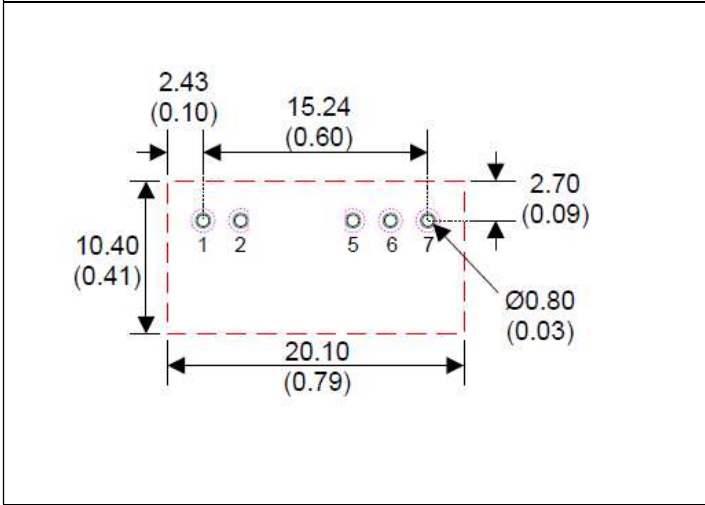
2 mm Pin pitch



- Notes : All dimensions are typical in millimeters (inches).
- 1. Pin dimension tolerance: ± 0.05 (± 0.002)
 - 2. Pin pitch and length tolerance: ± 0.35 (± 0.014)
 - 3. Pin to case tolerance: ± 0.5 (± 0.02)
 - 4. Case Tolerance: ± 0.5 (± 0.02)

Recommended Footprint Details

<u>Standard Pin pitch</u>	<u>2 mm Pin pitch</u>
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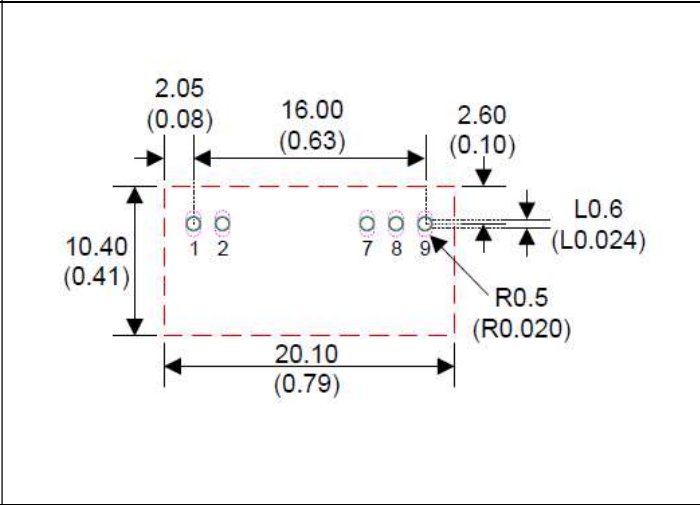
Notes : All dimensions are typical in millimeters (inches).

1. Through hole (black) : $\varnothing 0.80$ (0.031)
2. Top view pad (green) : $\varnothing 1.00$ (0.039)
3. Bottom view pad (pink) : $\varnothing 1.60$ (0.063)
pad 2 to pad 5 spacing: 6.00 (0.236)

There should be at least 6mm distance between primary and secondary circuit.

There should be at least 6mm distance between primary and secondary circuit.

2 mm Pin pitch



Notes : All dimensions are typical in millimeters (inches).

1. Through hole (black) : $\varnothing 0.90$ (0.035)
2. Top view pad (green) : $\varnothing 1.00$ (0.039)
3. Bottom view pad (pink) : Groove R0.50 L0.80
Groove (0.020) (0.031)

There should be at least 9mm distance between primary and secondary circuit.

There should be at least 9mm distance between primary and secondary circuit.

Notes

The information and specification contained in this data sheet are believed to be correct at time of publication. However, Acal BFi accepts no responsibility for consequences arising from printing errors or inaccuracies. Specifications are subject to change without notice.